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Interrupts as threads

S Kleiman, J Eychholt - ACM SIGOPS Operating Systems Review, 1995 - portal.acm.org

... Kernel threads use synchronization primitives that support protocols for preventing priority inversion, so a **thread's priority** is determined by which activities it is ... If an **interrupt** thread encounters a locked synchronization variable, it blocks and allows the critical section to clear. ...

Cited by 29 - Related articles - BL Direct - All 3 versions

[CITATION] IBM Power5 chip: A dual-core multithreaded processor

R Kalla, B Sinharoy, JM Tendler - IEEE micro, 2004

Cited by 339 - Related articles - BL Direct - All 38 versions

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[CITATION] Priority inversions in real-time communication

H Tokuda, CW Mercer, Y Ishikawa, TE Marchok - Real Time Systems Symposium, ..., 1989

Cited by 45 - Related articles - All 2 versions

Apparatus and method for interrupt handling in a multi-threaded operating system ...

SR Kleiman - US Patent 5,515,538, 1996 - Google Patents

... The kernel makes use of **prepared interrupt** handler threads for additional efficiency, and these **interrupt** handler threads are not subjected to inordinate delays caused by the phenomenon of **interrupt priority** inversion if they do become blocked. ...

Cited by 54 - Related articles - All 2 versions

Evaluation of real-time synchronization in real-time mach

H Tokuda, T Nakajima - Proceedings of the USENIX Mach Symposium, 1991 - usenix.org

... **Basic Priority (BP)**: All operations of this policy object are null functions. The waiting threads are enqueued in the lock object based on the **thread's priority**. ... **Interrupt** handler can be interrupted by a higher **priority interrupt**. CTM *A is the time to wakeup a blocked thread. ...

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[PDF] CPU inheritance scheduling

B Ford, SR Susarla - ACM SIGOPS Operating Systems Review, 1996 - Citeseer

... necessary to implement this framework does not have any notion of **thread priority**, CPU usage ... For example, a **fixed-priority** multi-processor scheduling policy can be implemented by main ... while waiting for an interesting event such as quantum expiration (eg, a clock **interrupt**). ...

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[PDF] Using Windows NT for real-time applications: Experimental observations ...

K Ramamritham, C Shen, O González, S Sen, ... - Proceedings of the ..., 1998 - Citeseer

... threads are allowed to mask and unmask **interrupts**, even an unimportant **interrupt** can adversely ... Our experiments were targeted towards the behavior of threads at **REALTIME priority** class and ... To this end, we used two threads both with same **thread priority** in the **REALTIME** ...

Cited by 67 - Related articles - View as HTML - All 28 versions

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Mach: a system software kernel

RF Rashid, H Tokuda - Computing Systems in Engineering, 1990 - Elsevier

... It contains no built-in file system or other higher level facilities which could interfere with **interrupt** handling or real-time ... When a higher **priority** thread blocks on the lock primitive, the current running lower **priority** thread will inherit the blocked **thread's priority** while running in ...

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[PDF] An ORB endsystem architecture for statically scheduled real-time ...

DC Schmidt, R Bector, D Levine, S Mungee, G ... - Proceedings of the ..., 1997 - Citeseer

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... To minimize **priority** inversion, the I/O threads perform protocol processing tasks for the ... Figure 6 illustrates how **thread-priority** inversions are alleviated in TAO's I/O subsystem by ... threads and STREAMS protocol kthreads and minimizing the work done in the **interrupt** context. ...

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[PDF] SunOS multi-thread architecture

ML Powell, SR Kleiman, S Barton, D Shah, D ... - Proceedings of the ..., 1991 - Citeseer

... each thread: Thread ID Register state (including PC and stack pointer) Stack Signal mask **Priority** Thread-local ... As each new **interrupt** comes in, another thread is chosen to handle the signal until all ... The initial **thread priority** and signal mask is set to the same values as its creator ...

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Interrupts as threads

S Kleiman, J Eyrkholt - ACM SIGOPS Operating Systems Review, 1995 - portal.acm.org

... the relative **priority** of processes within the class, and converts that **priority** to a **global priority**. ...
 kernel has been made preemptable to better support the real-time class and **interrupt** threads ...
 means that a lamnable thread rims as soon as is practical after its **priority** becomes **high** ...

Cited by 29 - Related articles - BL Direct - All 3 versions

[CITATION] The design and performance of a real-time I/O subsystem

F Kuhns, DC Schmidt, DL Levine - ... Symposium, 1999. Proceedings of the Fifth IEEE, 1999

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Apparatus and method for interrupt handling in a multi-threaded operating system ...

SR Kleiman - US Patent 5,515,538, 1996 - Google Patents

... of a process or its "state" is defined as: its text, values of **global** user variables ... As a result of using
interrupt handler threads for the entire **interrupt** processing task, and as a ... by other threads, **priority**
 inversion is possible (ie, a lower **priority** activity blocking a **higher priority** activity). ...

Cited by 54 - Related articles - All 2 versions

Vassal: Loadable scheduler support for multi-policy scheduling

GM Candea, MB Jones - Proceedings of the 2nd USENIX Windows NT ..., 1998 - usenix.org

... the primary scheduling policy, which is a **priority** scheduler, with round-robin execution within
 each **priority**. ... However, this **global** scheduler may reclaim the CPU from any given strand, therefore
 no ... Layer (HAL), which does not provide an interface to cause an **interrupt** at a ...

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[PDF] A library implementation of POSIX threads under UNIX

F Mueller - Proceedings of the USENIX Conference, 1993 - Citeseer

... When the thread regains control it will return from the **universal** signal handler, enable
 all signals again, and return to the UNIX **interrupt** frame which will restore the **global** state
 (**global** registers, floating point registers, and the the status word). ...

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Real time thread dispatcher for multiprocessor applications

JE Zolnowsky - US Patent 5,826,081, 1998 - Google Patents

... selected for execution, the processor proceeds to verify against threads in the **global** real time ...
interrupt routines, the holder of the schedule lock runs at an elevated **interrupt** level ..., hold unbound
 real time threads with **priority** higher than some predetermined **threshold priority** level ...

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[CITATION] Adaptive rate-controlled scheduling for multimedia applications

DKY Yau, SS Lam - IEEE/ACM transactions on networking, 1997

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[PDF] An ORB endsystem architecture for statically scheduled real-time ...

DC Schmidt, R Bector, D Levine, S Mungee, G ... - Proceedings of the ..., 1997 - Citeseer

... integrated with TAO's ORB-level thread-per-rate concurrency model to alleviate scheduling
 hazards such as **priority** inversion and ... The **global** priorities of all threads in this RTIO class are
higher than those of all other scheduling classes, except those of the **Interrupt** class. ...

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[PDF] Using Windows NT for real-time applications: Experimental observations ...

K Ramamritham, C Shen, O González, S Sen, ... - Proceedings of the ..., 1998 - Citeseer

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... 2. The device completes the /Operation and interrupts. The device driver then services the **interrupt**. ... This is a process running at the highest real-time **priority**. ... It initializes **global** events on the operator side and spawns 2 processes: 1. Heartbeat timer. 2. Main operator process. ...

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Evaluation of real-time synchronization in real-time mach

H Tokuda, T Nakajima - Proceedings of the USENIX Mach Symposium, 1991 - userix.org

... **rtjnutex-policy**-acquired checks the ceiling prior-ity of locks in the **global** lock queues ... For instance, **priority** ceiling algorithm is separated into **priority** inheritance management and **priority** ceiling management part ... **cnonlftt Cy_l.ft Thre«dB WQ^W Q W W Interrupt** handler C C IfiCjtar ...

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Interrupts as threads

S Kleiman, J Eykholt - ACM SIGOPS Operating Systems Review, 1995 - portal.acm.org

... support protocols for preventing **priority inversion**, so a **thread's priority** is determined by which activities it is ... relative **priority** of pro- cesses within the class, and converts that **priority** to a **global priority**. ... made preemptable to better sup- port the real-time class and **interrupt** threads. ...

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[CITATION] The design and performance of a real-time I/O subsystem

F Kuhns, DC Schmidt, DL Levine - ... Symposium, 1999. Proceedings of the Fifth IEEE, 1999

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Apparatus and method for interrupt handling in a multi-threaded operating system ...

SR Kleiman - US Patent 5,515,535, 1996 - Google Patents

... to inordinate delays caused by the phenomenon of **interrupt priority** inversion if ... REG %4 COMMON TRAP ENTRY: • FIND KERNEL STACK • SAVE **GLOBAL** REGISTERS • CHECK WINDOW OVERFLOW (SAVE IF REQUIRED) COMMON **INTERRUPT** CODE: • CHECK ...

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Vassal: Loadable scheduler support for multi-policy scheduling

GM Candea, MB Jones - Proceedings of the 2nd UNIX Windows NT ..., 1998 - unix.org

... the primary scheduling policy, which is a **priority** scheduler, with round-robin execution within each **priority**. ... However, this **global** scheduler may reclaim the CPU from any given strand, therefore no ... Layer (HAL), which does not provide an interface to cause an **interrupt** at a ...

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... integrated with TAO's ORB-level thread-per-rate concurrency model to alle- viate scheduling hazards such as **priority** inversion and ... The **global** priorities of all threads in this RTIO class are higher than those of all other scheduling classes, except those of the **interrupt** class. ...

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[CITATION] Adaptive rate-controlled scheduling for multimedia applications

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Fine-grain adaptive scheduling using feedback

H. Massalin, C. Pu - Computing Systems, 1989 - [usenix.org](#)

... In fact, a **global** adaptive scheduling algorithm may lower the **priority** of a CPU-intensive stage, making it the bottleneck and slowing down the whole pipeline. ... When the CPU quantum is exhausted, timer **interrupt** branches to the switch-out procedure in ...

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[CITATION] Niagara: A 32-way multithreaded sparc processor

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P Kongetira, K Alingarani, K Olukotun - IEEE micro, 2005

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APRIL: a processor architecture for multiprocessing

dtic.mi

A Agarwal, BH Lim, D Kranz, J ... - Proceedings of the 17th ... 1990 - portal.acm.org

... This simpli- fies processor design considerably because context switches can be more expensive (4 to 10 cycles), and functionality such as **scheduling** can be migrated into run-time software. ... The **trap** handler executes in the same task frame as the **thread** that trapped so ...

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Lightweight remote procedure call

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BN Bershad, TE Anderson, ED Lazowska, ... - ACM Transactions on ..., 1990 - portal.acm.org

... invoke Null() as a cross-domain operation involves one procedure call, followed by a kernel **trap** and change of the proces- sor's virtual memory context on call, and then a **trap** and context ... the general, slower **scheduling** path; instead, if the two concrete **threads** cooper- ating ...

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Scheduler activations: Effective kernel support for the user-level management of ...

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TE Anderson, BN Bershad, ED Lazowska, ... - ACM Transactions on ..., 1992 - portal.acm.org

... runs. - When a **thread** **traps** to the kernel to block (for example, because of a page fault), the processor on which the **thread** was running can be used to run another **thread** from the same or from a different address space. ... It is easy to change the policy for **scheduling** an ...

Cited by 699 - Related articles - All 193 versions

The MIT Alewife machine: A large-scale distributed memory multiprocessor

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A Agarwal, D Chaiken, GD'Souza, K ... - Scalable shared ..., 1991 - books.google.com

... Context switches are also forced when a **thread** encounters a **delay** due to a synchronization ... logic in the cache controller, and ex- tra complexity in the **thread scheduling** mechanism. ... meet several objectives: it must context **switch** rapidly; it must support fast **trap** dispatching; and ...

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Using continuations to implement thread management and communication in ...

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RP Draves, BN Bershad, RF Rashid, RW ... - Proceedings of the ..., 1991 - portal.acm.org

... model, the kernel's address space cent ains one stack for every **thread** in the system. When a **thread** **traps** into ... This saved information is used to later **resume** the blocked **thread** in an appropriate state. ... First, every ad- dress space still required at least one kernel-level **thread** ...

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Multiple threads in cyclic register windows

Y Hidaka, H Koike, H Tanaka - ACM SIGARCH Computer ..., 1993 - portal.acm.org

... Under adap- tive **scheduling**, the proposed scheme works well even for a small number of windows. ... it might be required to save another **thread's** window before restoring the missing win- dow of the current **thread** (see Figure 6). In Figure 6, an underjow **trap** from thread A ...

Cited by 29 - Related articles - BL Direct - All 3 versions

The PURE family of object-oriented operating systems for deeply embedded ...

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D Beuche, A Guerrouat, H Papajewski, W Schröder - ..., 1999 - computer.org

... 1. One way of operating the CPU is to let P URE run in- terruptedly. This family member merely supports low- level **trap**/interrupthandling. ... **scheduling** strategy code data **thread** **switch** time FCFS **thread** 2871 1052 94 FCFS same bundle 3391 1052 126 FCFS diff. ...

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... error number is restored; the requested per-thread signal mask is restored and **pending** signals on ... These two **traps** consume most of the time required for a context **switch** and are ... on a multiprocessor and wakeups due to asynchronous events may cause the **thread** to **resume** ...

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